OSTROLENK, FABER

MAR 0 5 2004

Ø001/002

OFFICIAL

P/4076-19

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Ching Man STANLEY

Date:

March 5, 2004

Serial No.:

10/083,177

Art Unit:

2858

Filed:

February 25, 2002

Examiner:

Paresh H. Patel

For:

METHOD AND APPARATUS FOR PROCESSING AN ARRAY

OF COMPONENTS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

VIA FACSIMILE (703) 872-9306

SUPPLEMENTAL SUBMISSION

Further in response to the Office Action mailed December 9, 2003, please reconsider the above-identified application in view of the following comments:

REMARKS

Applicant gratefully acknowledges the courtesy and assistance afforded to his representative by the Examiner during an interview held March 4, 2004.

In accordance with MPEP §713.04, the following is Applicant's summary of the substance of the interview.

During the interview, Applicant's representative pointed out that base claims 1 and 8 are now limited to "back end" processing, i.e., processing of arrays of packaged semiconductor devices. In contrast to this, the subject matter of the principle reference, Gotman, is directed to the handling of wafer level devices, i.e., "front end" processing. The significant differences between front end and back end processing were discussed with the Examiner.

00648571.1